

Tentative Merit List: Overall (All Categories)
M.Tech (Microelectronics) GATE
Department: UNIVERSITY INSTITUTE OF ENGINEERING & TECHNOLOGY
In case of any discrepancy regarding Marks/P.U. wtg./Hons. wtg. send mail to: uiet_pu@pu.ac.in by 04/08/2025

No.	Appl. No.	Name	Father's Name	DoB	Cat.	10th	12th	Qualy. Marks obt.	Qualy. Marks Total	GATE Score (1000)	GATE Roll. no.	Qualy. Norm. (0)	GATE(100)	Overall Marks	Remarks
1	2515012040	Yasman Kaur Sidhu	Darshan Singh Sidhu	23-04-2000	GEN	95.00	87.20	1553	2000	398	PH25S78032166	1863.6	39.8	39.8	Approved by Dept.
2	2515012470	Pooja Priyanka	Shiv Shanker Sundram	01-04-2000	GEN	84.60	60.60	74575	100000	288	EC25S64036889	1789.8	28.8	28.8	Approved by Dept.
3	2515000853	Dilraj Singh	Harpal Singh	15-08-2002	SC	63.30	70.00	7	10	264	EC25S68003101	1680	26.4	26.4	Approved by Dept. SC -Approved NCC- C -Approved,